



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Eliyahou Harari et al
Title: Flash EEPROM System
Application No.: 09/867,636 Filing Date: May 30, 2001
Examiner: Tran, Andrew Q. Group Art Unit: 2824
Docket No.: SNDK.A06US7 Conf. No.: 6307

09/867,836

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Mary E. Buggie
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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO THIRD REQUIREMENT FOR RESTRICTION

Sir:

In response to the non-final Office Action dated August 29, 2005 in the above-identified patent application, the claims of Group II (method claims 81-100) directed to Species A (two-level operation) are hereby elected for examination. These claims are: 81, 82, 84, 86-90, 91/81, 91/82, 91/84, 91/86-90, 92/84, 93/81, 93/82, 93/84, 95, 96/81, 96/82, 97/96/81, 97/96/82, 98/96/81, 98/96/82, 100/81 and 100/82.

Elected claims of Group II that are generic to both two-level (Species A) and multi-level (Species B) operation include claims 81, 86-90, 91/81, 91/86-90, 93/81 and 94/81.

This responds to the third Requirement for Restriction in this case, each Requirement dividing the claims into essentially the same groupings. Commencement at an early date of substantive examination of the present application, filed over 4 years ago, is respectfully

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requested.

Related to the present application are parent application serial no. 09/129,675, filed August 5, 1998, and divisional application serial no. 10/290,564, filed November 7, 2002, both currently assigned along with the present application to Examiner Andrew Tran.

Respectfully submitted,



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December 22, 2005

Date

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